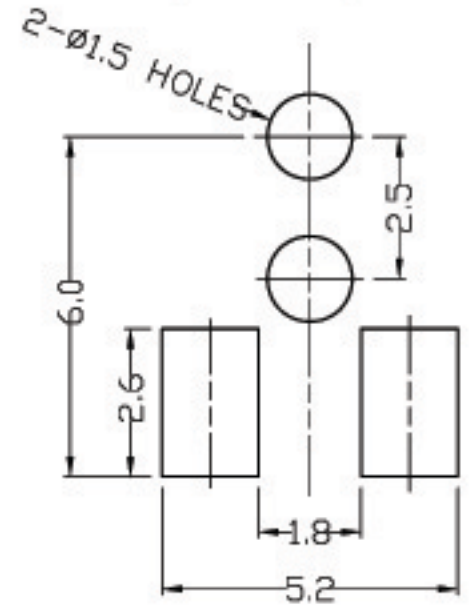


PCB LAYOUT



DIRCUIT DIAGRAM

